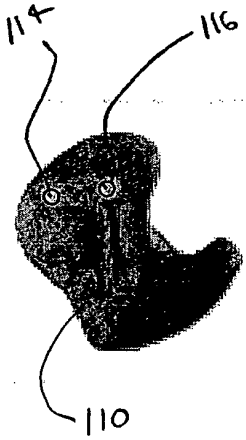
FIG. 1A



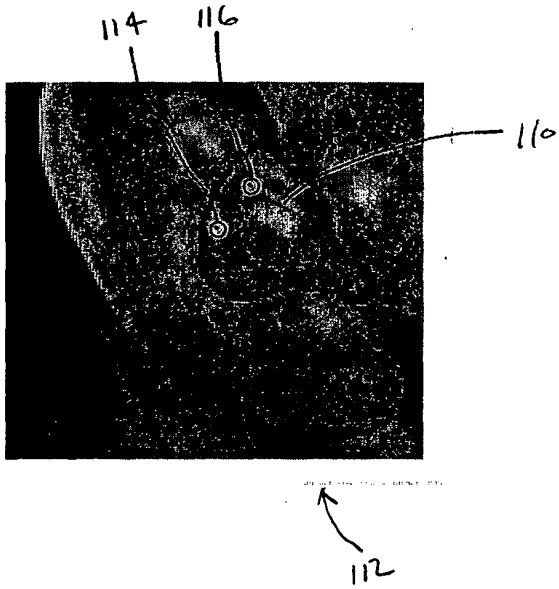
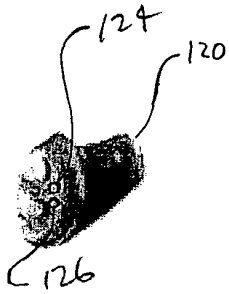


FIG. 1B



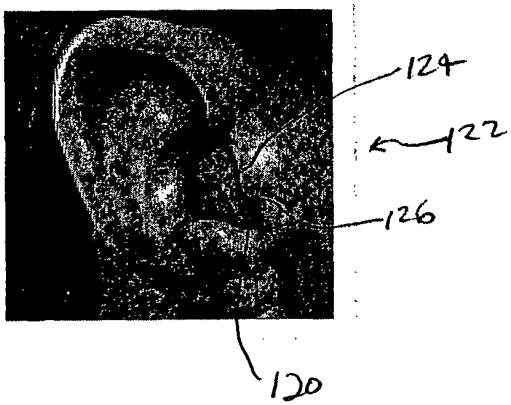


FIG. 1C

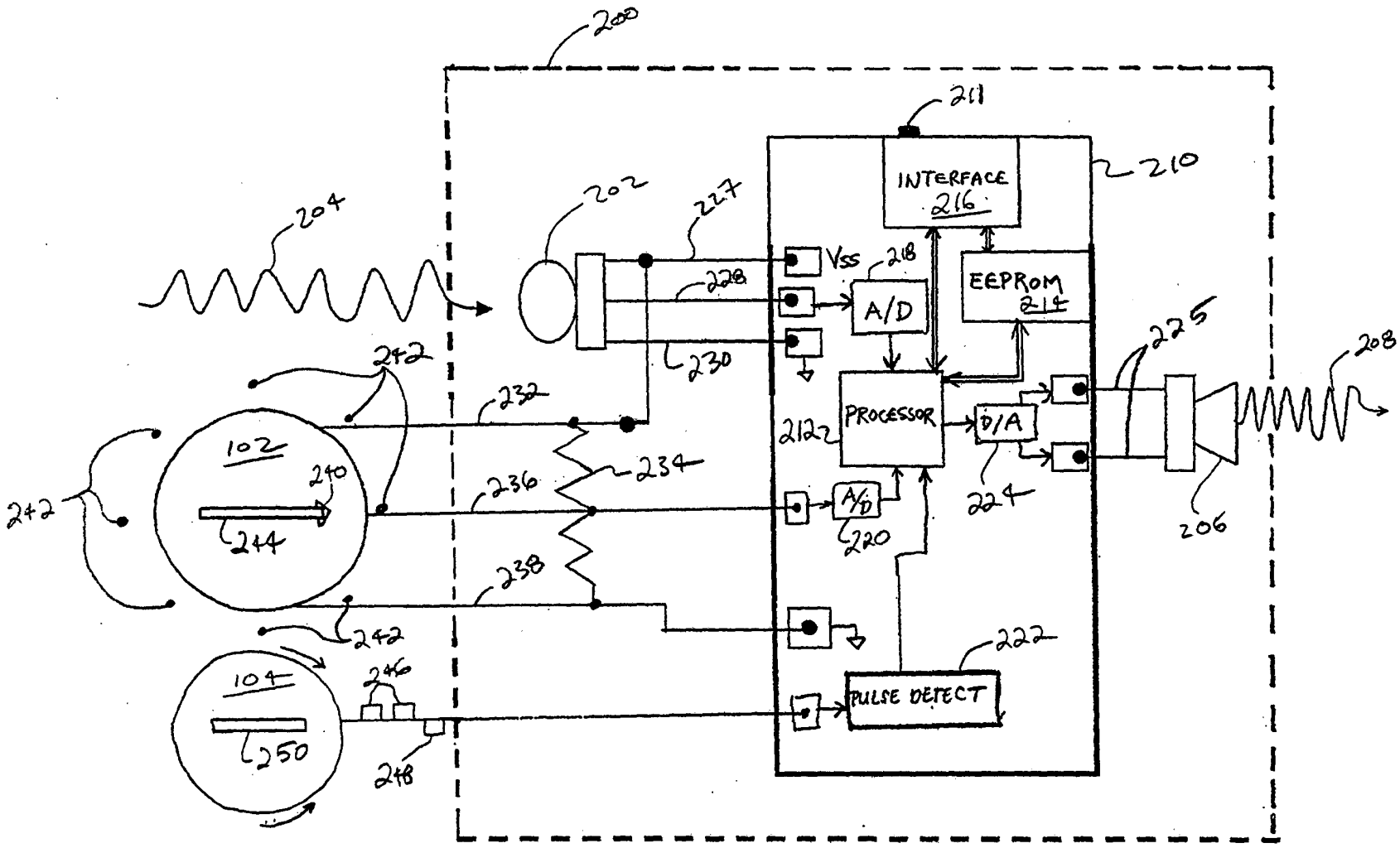

200
211
INTERFACE
216
210
204
202
227
228
230
Vss
218
A/D
EEPROM
214
225
208
242
232
102
240
234
236
212
PROCESSOR
D/A
244
A/D
220
224
206
238
242
222
104
246
PULSE DETECT
250
248

FIG. 2

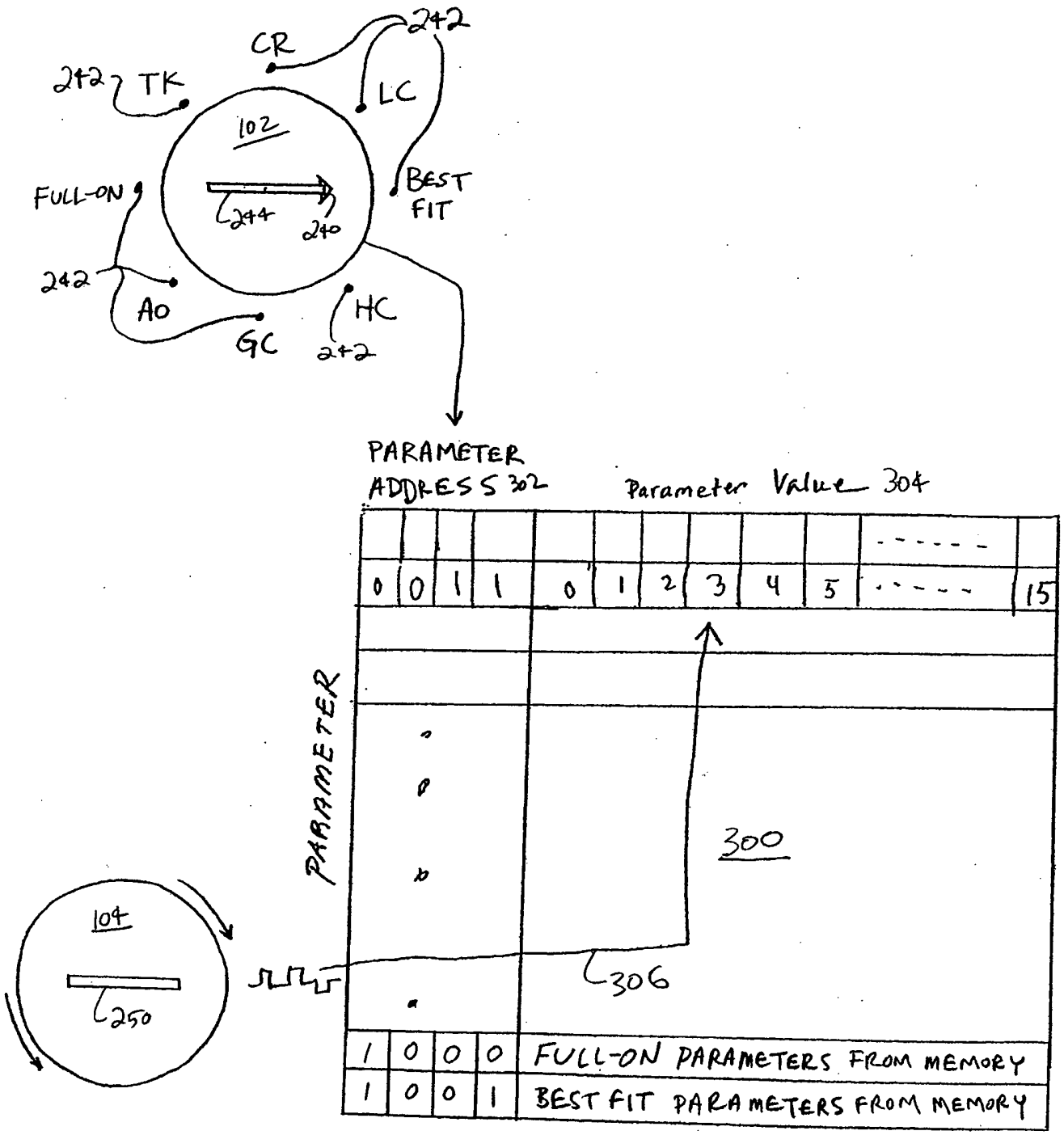

CR
242
242
TK
LC
102
FULL-ON
BEST FIT
244
240
242
AO
HC
GC
242
PARAMETER ADDRESS 302
Parameter Value 304
0 0 1 1 0 1 2 3 4 5 15
PARAMETER
300
104
250
306
1 0 0 0 FULL-ON PARAMETERS FROM MEMORY
1 0 0 1 BEST FIT PARAMETERS FROM MEMORY

FIG. 3

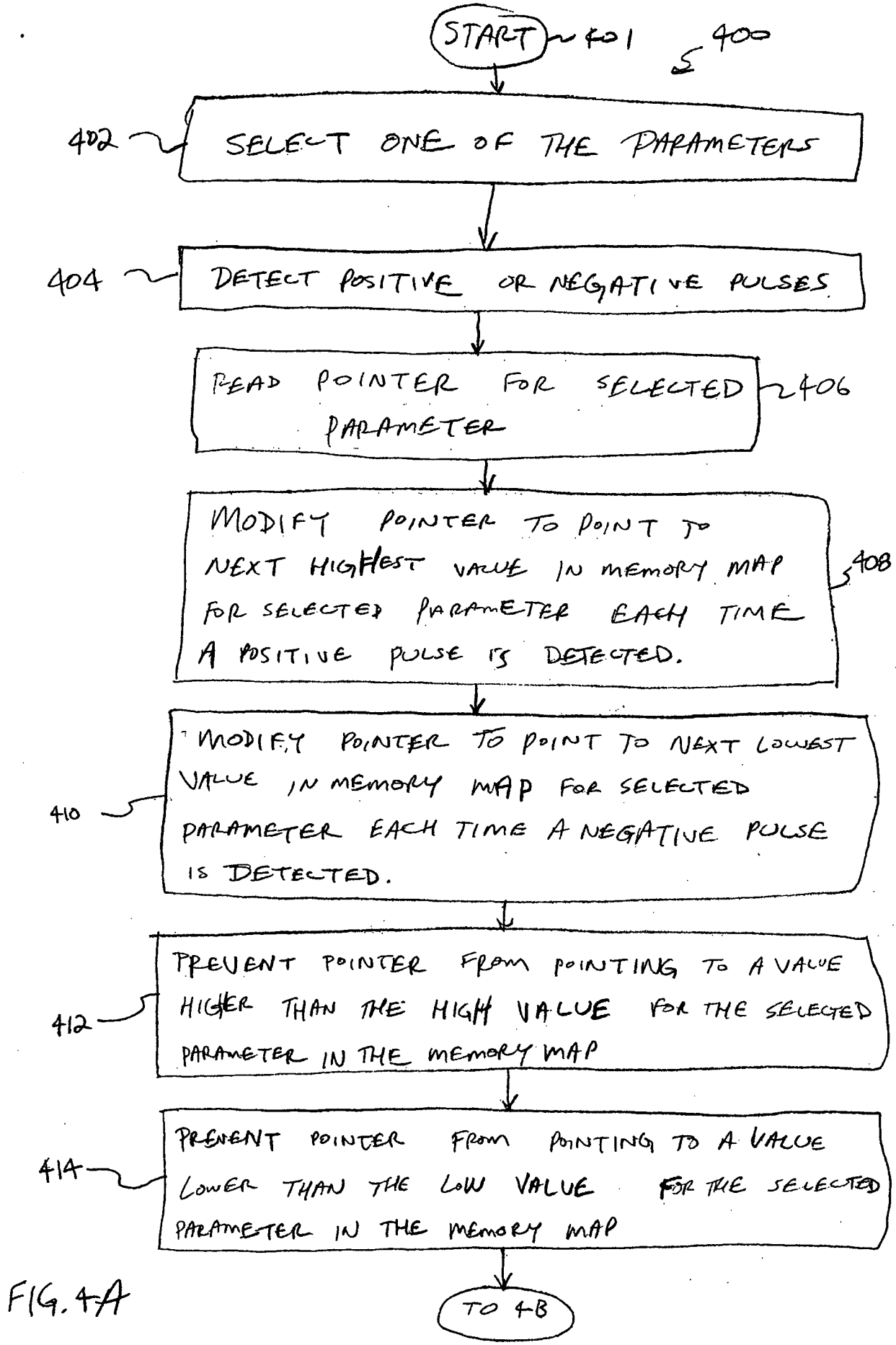

START
401
400
402
SELECT ONE OF THE PARAMETERS
404
DETECT POSITIVE OR NEGATIVE PULSES
READ POINTER FOR SELECTED PARAMETER
406
MODIFY POINTER TO POINT TO NEXT HIGHEST VALUE IN MEMORY MAP FOR SELECTED PARAMETER EACH TIME A POSITIVE PULSE IS DETECTED.
408
MODIFY POINTER TO POINT TO NEXT LOWEST VALUE IN MEMORY MAP FOR SELECTED PARAMETER EACH TIME A NEGATIVE PULSE IS DETECTED.
410
PREVENT POINTER FROM POINTING TO A VALUE HIGHER THAN THE HIGH VALUE FOR THE SELECTED PARAMETER IN THE MEMORY MAP
412
PREVENT POINTER FROM POINTING TO A VALUE LOWER THAN THE LOW VALUE FOR THE SELECTED PARAMETER IN THE MEMORY MAP
414
TO 4B

FIG. 4A

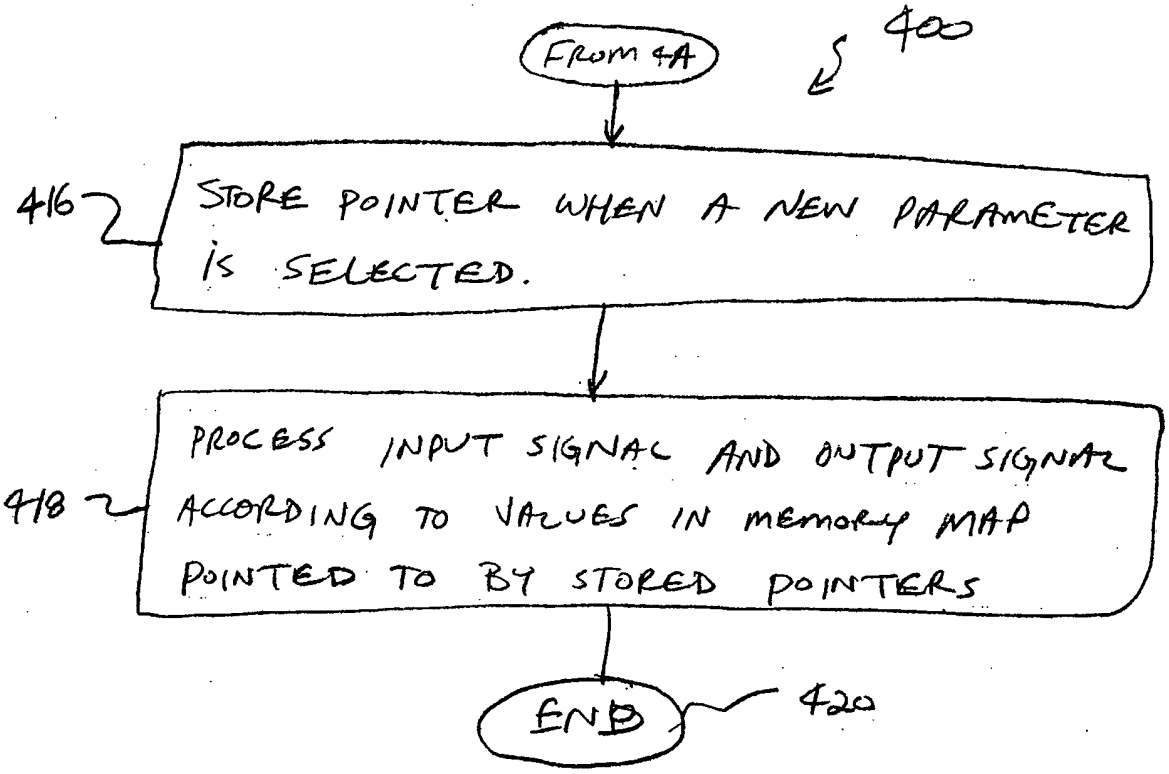
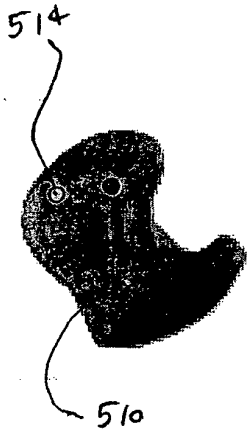

FROM 4A
400
416
STORE POINTER WHEN A NEW PARAMETER IS SELECTED.
418
PROCESS INPUT SIGNAL AND OUTPUT SIGNAL ACCORDING TO VALUES IN MEMORY MAP POINTED TO BY STORED POINTERS
END
420

FIG 4B

FIG. 5A



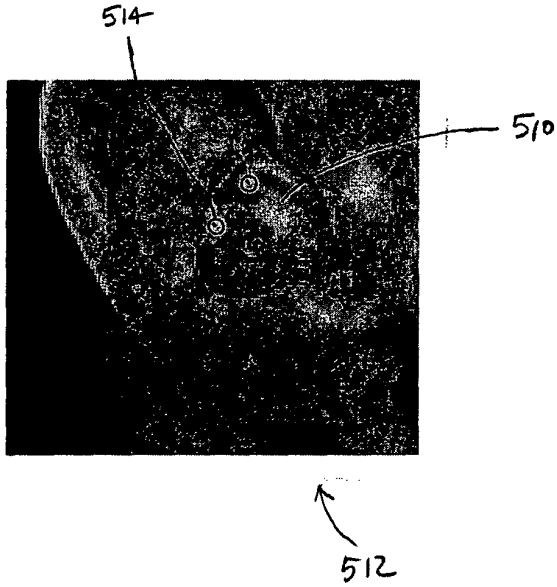
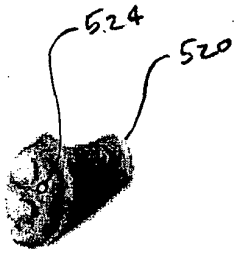


FIG. 5B



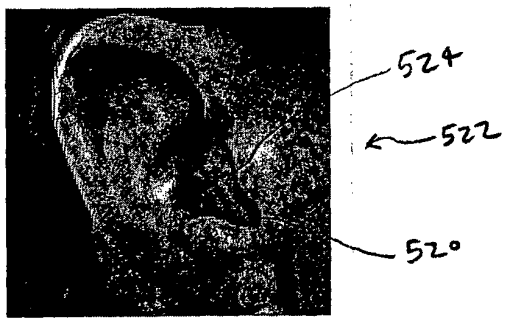


FIG. 5C

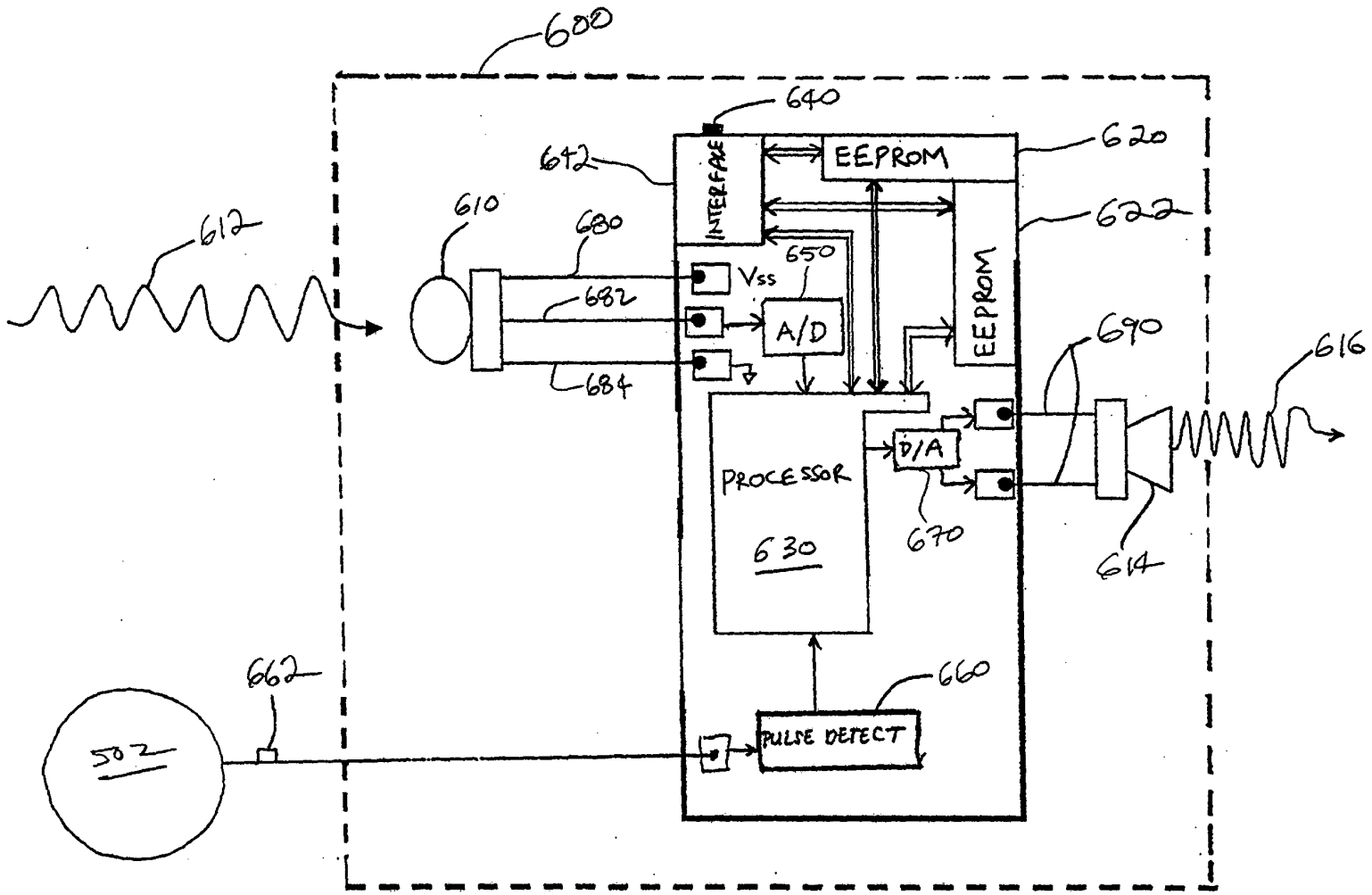

600
640
642
INTERFACE
EEPROM
620
622
612
610
680
650
Vss
682
A/D
EEPROM
690
616
684
PROCESSOR
630
D/A
670
614
662
660
PULSE DETECT
502

FIG. 6